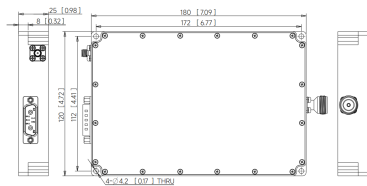


300 MHz–1.05 GHz 100 W Broadband RF Power Amplifier Module

This 300–1050 MHz stage specifies 100 W typical saturated output and 50 dB typical gain. Typical gain flatness is ± 1 dB; at 100 W, typical current is 6 A and power-added efficiency is 45%.



Electrical Specifications · +28.0 VDC, 25 °C, 50 Ω system unless otherwise stated

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Operating frequency	BW	300	-	1050	MHz
Saturated RF output power	Pout	-	100	-	W
Power gain	Gp	-	50	-	dB
Power gain flatness	Gp	-	± 1	-	dB
VSWR	S11	-	1.5	-	-
Harmonics at 100 W	H	-	-10	-	dBc
Spurious signals	Spur	-	-60	-	dBc
Input/output impedance	Impedance	-	50	-	Ω
Operating voltage	VDC	24	28	32	VDC
Power-added efficiency	Eff	-	45	-	%
Current consumption at Pout = 100 W	IDD	-	6	-	A

Monitoring and Protection

Parameter	Specification
Monitoring	Voltage, current, output power, reflected power, and temperature
Output VSWR protection	Amplifier shutdown at output VSWR between 3:1 and 3.5:1
Over-temperature protection	Amplifier shutdown above 85 °C
Supply protection	Shutdown above 32 V, below 24 V, or with accidental reverse wiring
Over-current protection	Amplifier shutdown above 9 A

Absolute Maximum Ratings · Exceeding these ratings may damage the module

Parameter	Rating
RF input drive without damage	+10 dBm
Load VSWR at Pout = 100 W	3:1 at all load phases, continuous
Thermal overload	85 °C shutdown

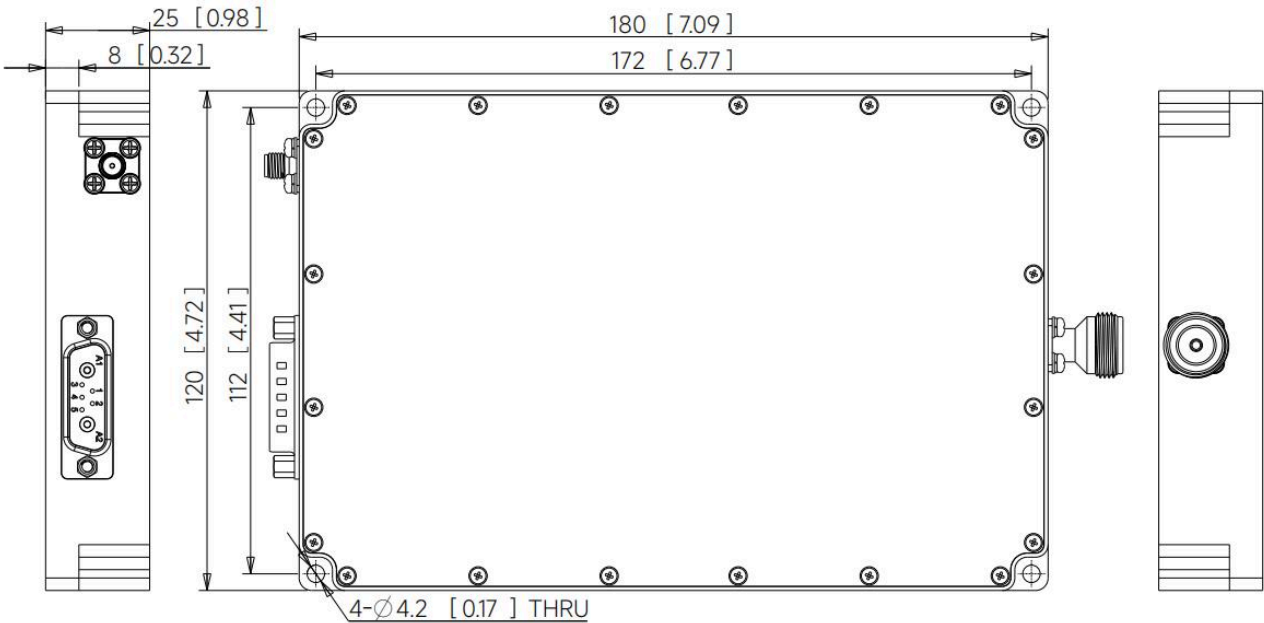
Environmental Specifications · Design targets; project confirmation required

Parameter	Minimum	Typical	Maximum	Unit
Operating temperature	-20	-	50	°C
Storage temperature	-25	-	65	°C
Relative humidity (non-condensing)	-	-	95	% RH

Mechanical Specifications

Parameter	Value	Unit
Dimensions	180 × 120 × 25[7.09 × 4.72 × 0.98] (Maximum)	mm [in]
Weight	2[4.4] (Maximum)	kg [lb]
RF input connector	SMA female	-
RF output connector	N female	-
DC/control interface	Hybrid D-Sub 7-pin male	-

Mechanical Outline



All dimensions are in mm [in].

DC and Control Interface

Pin #	Description	Specification
A1	A1 GND	Ground
A2	A2 VDD	+28V
1	1 ENABLE	TTL logic high (3.3 V); internally pulled low
2	2 GND	Ground
3	3 485A	RS-485 serial communication
4	4 485B	RS-485 serial communication
5	5 NC	Not connected

Document notice

Specifications are subject to revision. Before design release, confirm the current document revision and product-specific requirements with LCRF. The approved quotation and order documentation define the supplied configuration.