300–1100 MHz One-Input Two-Output Amplifier



The verified functional diagram shows IN1 through one amplifier to OUT1 and OUT2. The source table gives 300–1100 MHz operation, 30 dB typical gain and 8 dBm typical Psat. Noise figure is not stated.

Typical supply is 5 V and typical module current 100 mA. The mechanical table specifies 50 × 40 × 13 mm and SMA-K female RF connectors. The displayed asset is a functional schematic, not an exact hardware outline.

Electrical specifications

Parameter	Value	Unit
Operating frequency	300–1100	MHz
Gain	30 (Typical)	dB
Saturated output power (Psat) [1]	8 (Typical)	dBm
Input S11 (signed source value) [2]	−10 (Typical)	dB
Supply voltage	5 (Typical)	VDC
Dynamic current	100 (Typical)	mA

[1] Saturated output (Psat), not P1dB or guaranteed linear output.

[2] Negative S11 convention retained from source; not recast as positive return loss.

Absolute maximum ratings

Parameter	Value	Unit
Absolute maximum supply voltage	≤7 (Absolute maximum rating; continuous operation or performance at this limit is not established. Maximum: 7 VDC.)	VDC
Absolute maximum input power	≤12 (Absolute maximum rating; continuous operation or performance at this limit is not established. Maximum: 12 dBm.)	dBm
Operating temperature	−40–+70	°C
Storage temperature	−40–+85	°C

Interfaces and contacts

Interface	Specification
RF input connector	SMA-K female
RF output connector	SMA-K female
DC supply interface	Feed-through capacitor
Ground interface	Grounding metal sheet

Interfaces and contacts

Interface	Specification
RF input port count	1
RF output port count	2
Signal routing	One input amplified and distributed to two outputs

Mechanical specifications

Parameter	Value	Unit
Body dimensions	50 × 40 × 13	mm

Document notice

Specifications are subject to revision. Before design release, confirm the current document revision and product-specific requirements with LCRF. The approved quotation and order documentation define the supplied configuration.