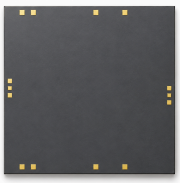
92000–96000 MHz MMIC Power Amplifier Die

LCI-PA-92000-96000-PS36T is a MMIC power amplifier supplied as bare die for 92000–96000 MHz.

PSAT: 36 dBm (typical); PAE: 9 % (typical); Small-signal gain: 19 dB (typical). Die dimensions: 5.1 × 5.1 × 0.05 mm.



RF characteristics

TA = +25 °C; Vd = +18 V; Id = 1.2 A

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Operating frequency	f	92000	-	96000	MHz
Small-signal gain	S21	-	19	-	dB
Small-signal gain flatness	ΔS21	-	±1	-	dB
Saturated output power	Psat	-	36	-	dBm
Power-added efficiency	PAE	-	9	-	%
Input VSWR	VSWR(in)	-	2	-	-
Output VSWR	-	-	2	-	-
Saturation current	-	-	2.8	3.2	A

DC bias and current

Parameter	Symbol	Minimum	Typical	Maximum	Value	Unit
Drain voltage [1]	Vd	-	-	-	18	V
Gate voltage [1]	Vg	-	-	-	-1.6	V
Bias current [2]	-	-	-	-	1.2	A

[1] Value stated without a Min/Typ/Max qualification.

[2] Vd = +18 V; Vg = -1.6 V; Value stated without a Min/Typ/Max qualification.

Absolute limits

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Maximum drain-source voltage	Vd	-	-	19	V
Minimum gate bias	Vg	-5	-	-	V
Maximum input power	Pin	-	-	30	dBm
Storage temperature	TSTG	-65	-	150	°C

Environmental specifications

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Operating temperature	-	-55	-	85	°C

Die dimensions

Parameter	Value	Unit
Die dimensions	5.1 × 5.1 × 0.05	mm

Current publication record · 1.7 · 2026-11-12. Product description or editorial content updated. Replaces revision 1.6.

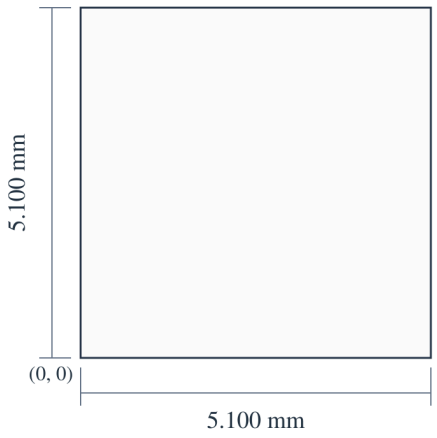
Die integration and bias sequencing

Separate characterization: TA = +25 °C; Vd = +18 V; Id = 1.2 A. Apply gate bias before drain bias. Remove drain bias before gate bias.

Mechanical Outline

Outline and dimensioned coordinates in mm. Use this drawing as a dimensional reference. Fabrication and wire bonding require complete drawings.

LCI-PA-92000-96000-PS36T



X (mm)
Vg (3): 0.820
(4/6): 2.740
(5/7): 3.630

Y (mm)
RFin: 2.700
RFout: 2.400

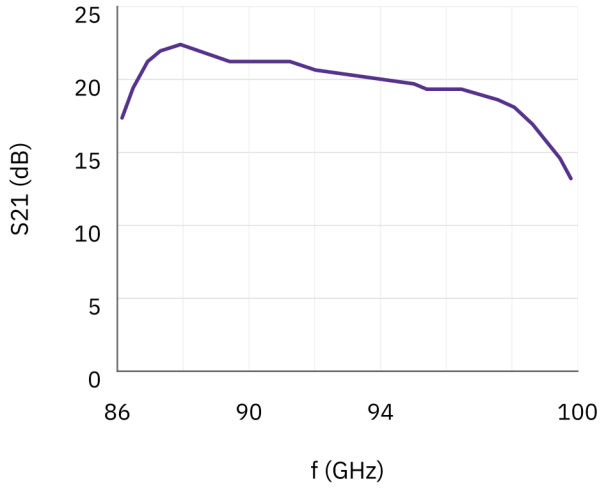
5.100 × 5.100 × 0.050 mm
X, Y: ±0.050 mm
X → · Y ↑ · X, Y: mm

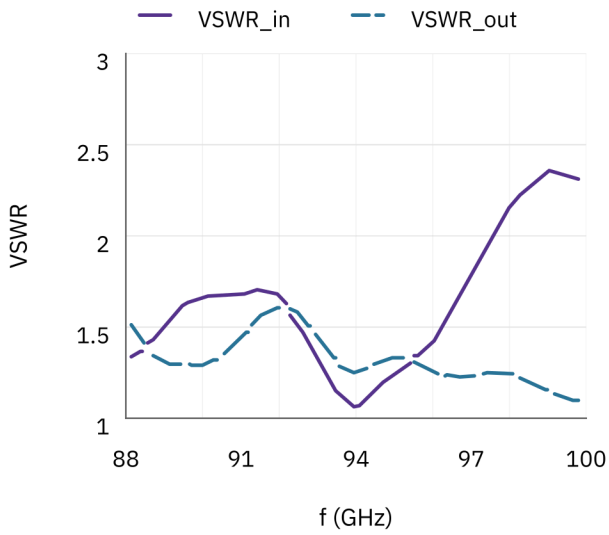
Performance curves

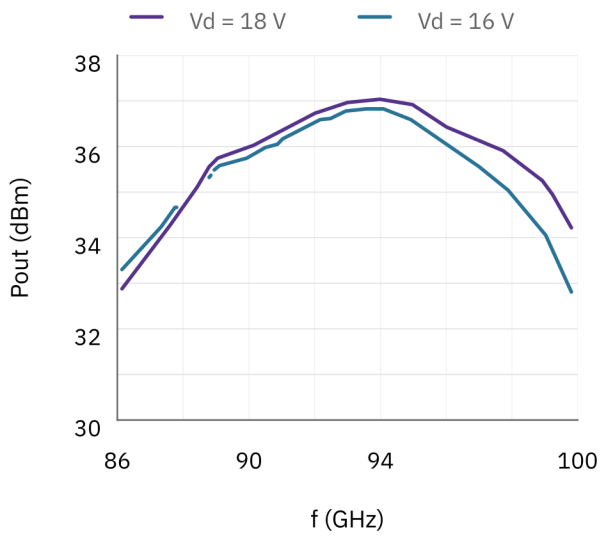
Each performance plot applies to its stated operating conditions.

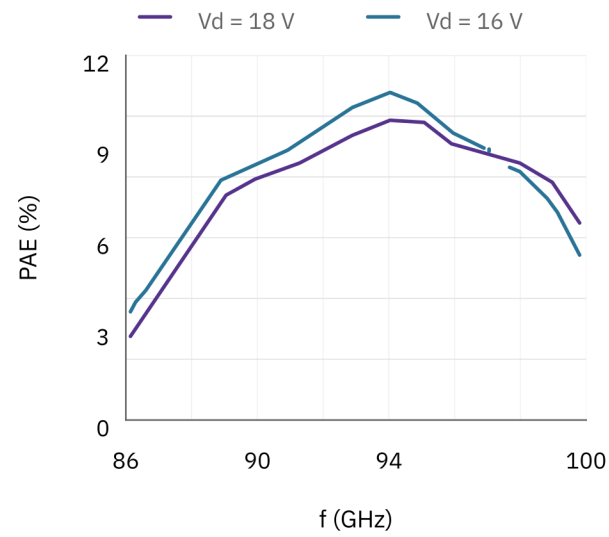
Small-signal gain; Input matching; Output matching; Output power; Power-added efficiency.

Figure notes: TA=+25°C; Vd=18V; Id=1200mA. Readings are approximate. Pout / PAE: Pin = 25 dBm; Vd = 18 / 16 V. Read each trace with the conditions shown in its plot.









Document notice

Specifications are subject to revision. Before design release, confirm the current document revision and product-specific requirements with LCRF. The approved quotation and order documentation define the supplied configuration.