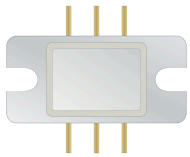
7500–10500 MHz GaN Packaged Power Amplifier — PSAT 48 dBm (typical)

LCI-PA-7500-10500-PS48T · 7500–10500 MHz · GaN Packaged Power Amplifier.

The RF table specifies: PSAT 48 dBm (typical); Power gain 24 dB (typical); PAE 40 % (typical). RF test conditions: Vd = +28 V; Vg = -2.6 V; D = 10%; Pin = 24 dBm.



CMW010B

Apply gate bias before drain bias. Remove drain bias before gate bias.

Vg: C = 10 µF

Vg: 1, 3

RFin: 2

Vd: 4, 6

RFout: 5

Package outline dimensions: 18.03 × 8.7 mm.

RF characteristics

Vd = +28 V; Vg = -2.6 V; D = 10%; Pin = 24 dBm

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Operating frequency	f	7500	-	10500	MHz
Saturated output power	-	47.5	48	-	dBm
Power gain	-	23.5	24	-	dB
Power-added efficiency	-	32	40	-	%
Power-gain flatness	-	-	0.5	1	dB

DC bias and current

Parameter	Symbol	Minimum	Typical	Maximum	Value	Unit
Drain voltage	-	-	-	-	28	V
Gate voltage	-	-	-	-	-2.6	V

Absolute limits

Absolute limits; these are not normal operating targets.

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Maximum drain-source voltage	-	-	-	32	V
Minimum gate bias	-	-5	-	-	V
Storage temperature	-	-65	-	150	°C
Maximum input power	-	-	-	28	dBm

Absolute limits

Absolute limits; these are not normal operating targets.

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Maximum channel temperature	-	-	-	200	°C

Package outline dimensions

Parameter	Value	Unit
Package outline dimensions	18.03 × 8.7	mm
Terminal span	14.7	mm

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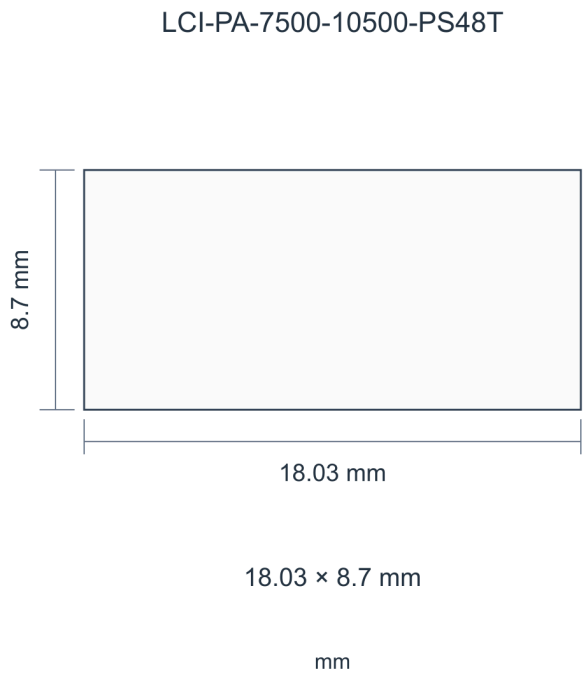
Integration notes

CMW010B Apply gate bias before drain bias. Remove drain bias before gate bias. Vg: C = 10 µF Vg: 1, 3 RFin: 2 Vd: 4, 6 RFout: 5

Mechanical Outline

Partial drawing showing confirmed dimensions in mm. Use the complete installation drawing for installation.

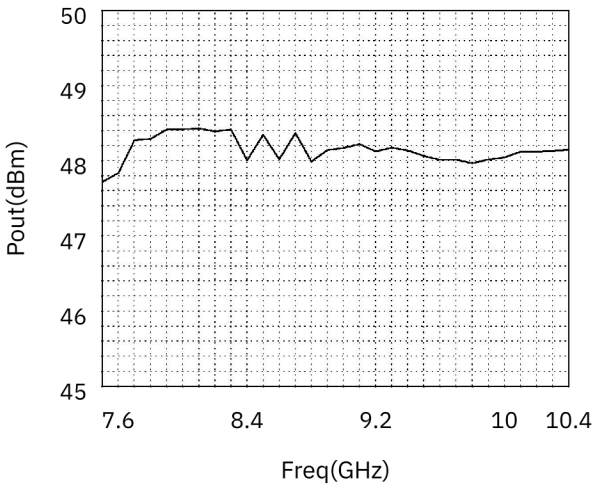


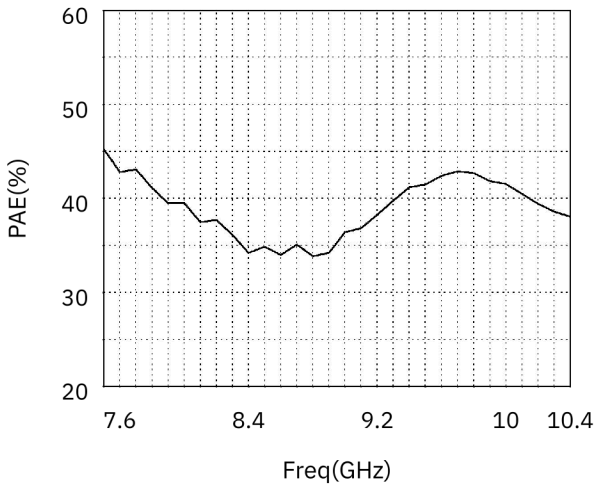
Performance curves

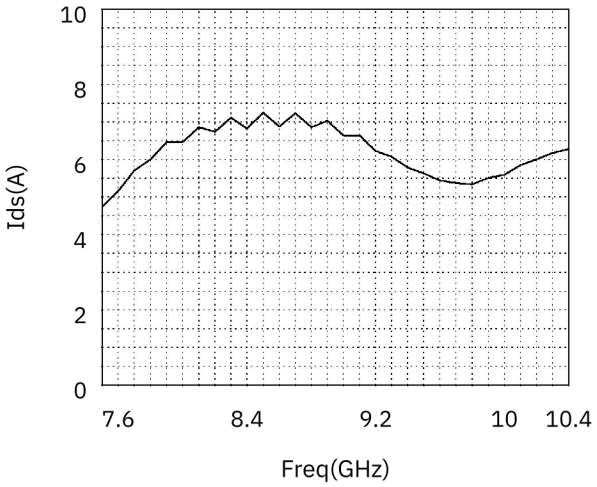
Each performance plot applies to its stated operating conditions.

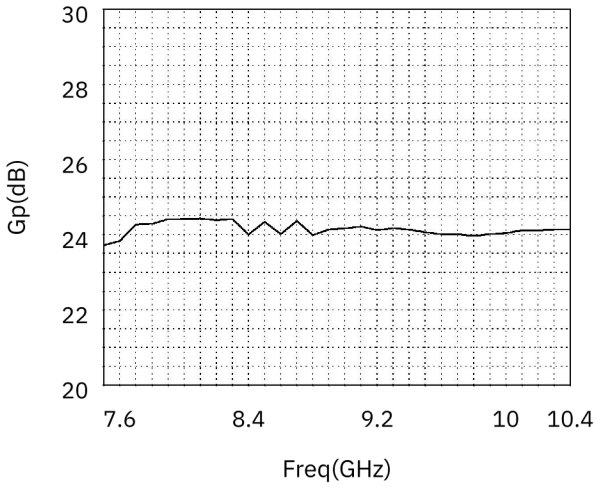
Saturated output power; Power-added efficiency; Dynamic drain current; Power gain.

Figure notes: TA=+25°C; Vd=+28V; Vg=-2.6V; Pin=24dBm. D = 10%.









Document notice

Specifications are subject to revision. Before design release, confirm the current document revision and product-specific requirements with LCRF. The approved quotation and order documentation define the supplied configuration.