7000–13000 MHz GaN MMIC Power Amplifier Die

LCI-PA-7000-13000-PS48T is a GaN MMIC power amplifier supplied as bare die for 7000–13000 MHz. It supports CW and pulsed operation.

The RF table specifies: Saturated output power: 48 dBm (typical); Power gain: 24 dB (typical); Power-added efficiency: 40 % (typical). RF test conditions: TA = +25 °C; Vd = +28 V; Vg = -2.6 V. Die dimensions: 3.83 × 5.82 × 0.1 mm.



RF characteristics

TA = +25 °C; Vd = +28 V; Vg = -2.6 V

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Operating frequency	f	7000	-	13000	MHz
Saturated output power	Psat	-	48	-	dBm
Power gain	Gp	-	24	-	dB
Power-gain flatness	ΔGp	-	-	±1	dB
Power-added efficiency	PAE	-	40	-	%
Small-signal gain	S21	-	35	-	dB
Small-signal gain flatness	ΔS21	-	-	±1.5	dB
Input VSWR	VSWR(in)	-	1.6	2	-

DC bias and current

TA = +25 °C

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Drain voltage	Vd	-	28	32	V
Gate voltage	Vg	-3	-2.6	-2.5	V
Quiescent drain current	Id	-	2.6	-	A
Dynamic drain current	Idd	-	5.7	6.7	A
Dynamic gate current	Igg	-	0.2	0.4	mA

Absolute limits

Absolute limits; these are not normal operating targets.

Parameter	Symbol	Minimum	Typical	Maximum	Value	Unit
Maximum drain-source voltage	Vd	-	-	32	-	V
Minimum gate bias	Vg	-5	-	-	-	V
Maximum input power	Pin	-	-	27	-	dBm
Maximum channel temperature	Top	-	-	225	-	°C

Absolute limits

Absolute limits; these are not normal operating targets.

Parameter	Symbol	Minimum	Typical	Maximum	Value	Unit
Storage temperature	TSTG	-65	-	150	-	°C
Load mismatch survival	-	-	-	-	7:1	-

Die dimensions

Parameter	Value	Unit
Die dimensions	3.83 × 5.82 × 0.1	mm
Plan-view outline tolerance	±100 (Plan-view outline only; the source does not specify thickness tolerance.)	µm

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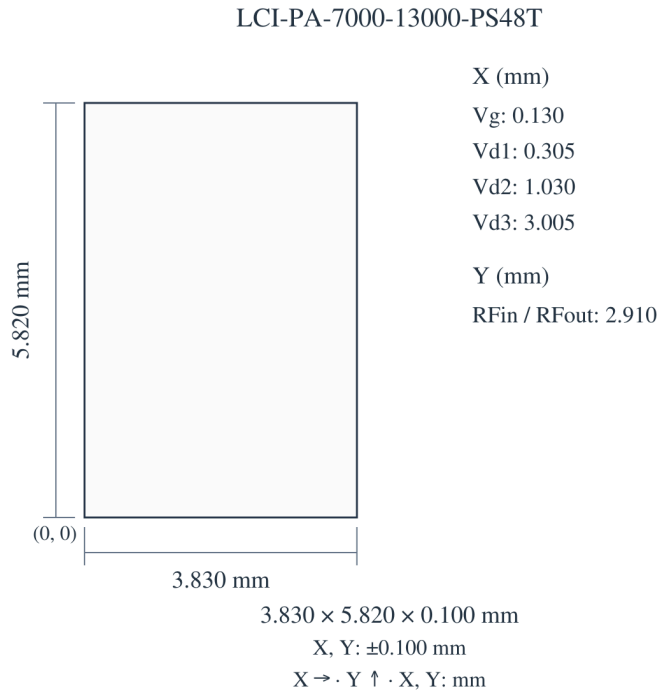
Die integration and bias sequencing

Apply gate bias before drain bias at power-up. At power-down, remove drain bias before gate bias. Use clean handling, ESD protection and a carrier with thermal expansion compatible with the SiC die.

Mechanical Outline

Outline and dimensioned coordinates in mm. Use this drawing as a dimensional reference. Fabrication and wire bonding require complete drawings.

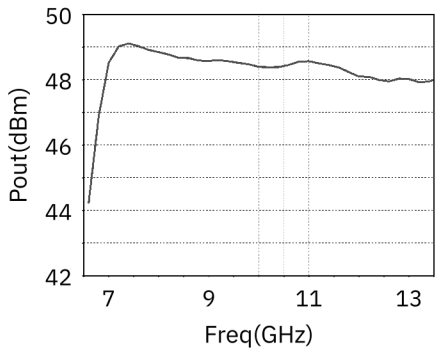


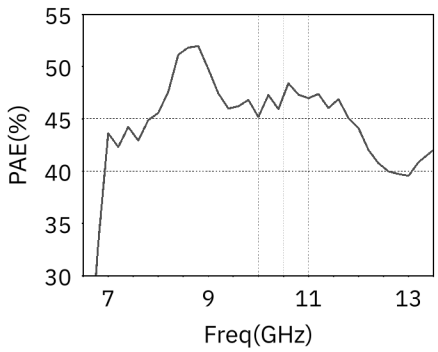
Performance curves

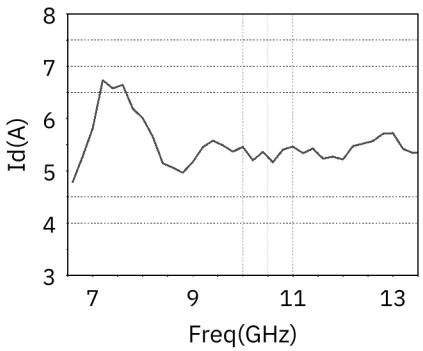
Each performance plot applies to its stated operating conditions.

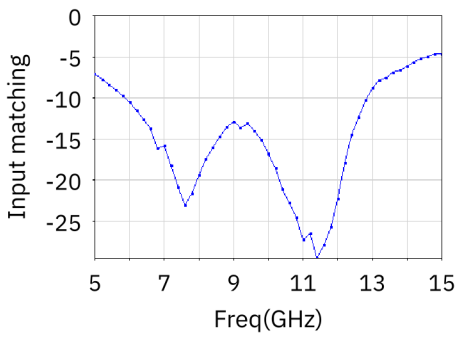
Pulsed — Saturated output power; Power-added efficiency; Dynamic drain current; Input matching; Gain.

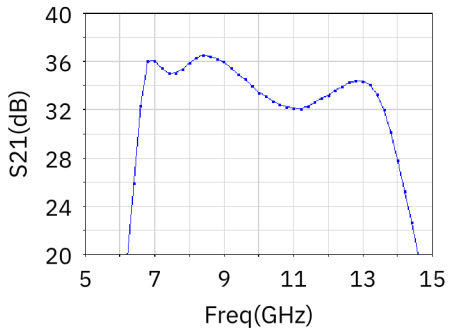
Figure notes: TA=+25°C; Vd=+28V; Vg=-2.6V. PW = 100 µs; D = 10%. Plots 1–3: Pin = 24 dBm. Plots 4–5: Pin = -10 dBm. The input-matching plot shows negative values on its vertical axis, with no specified unit or S11 designation.





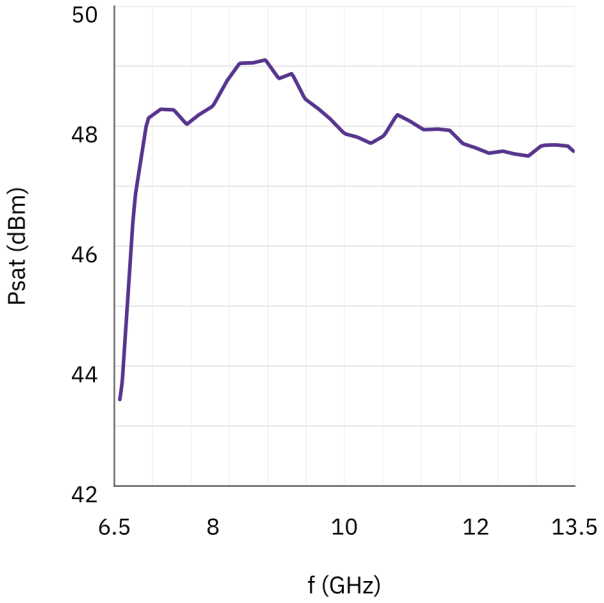


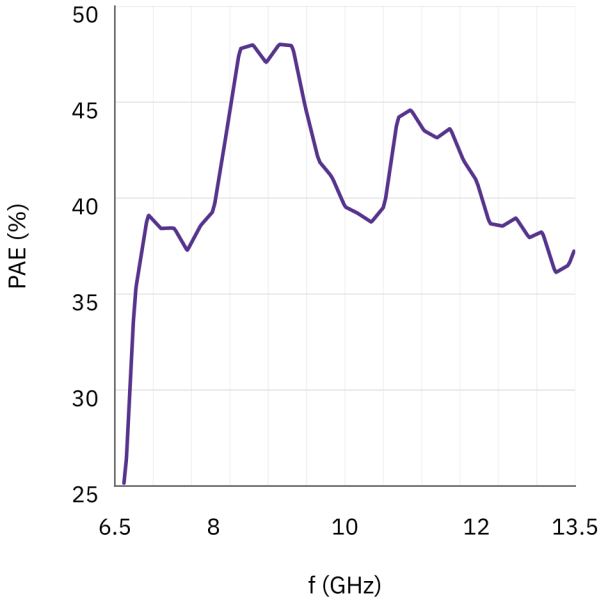


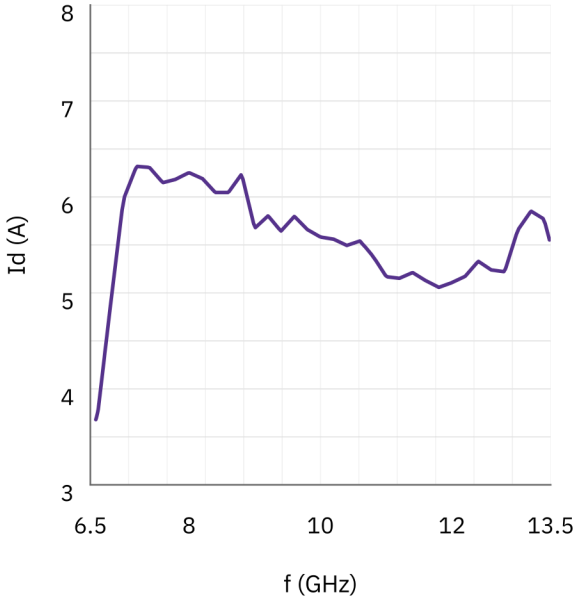


Continuous wave (CW) — Saturated output power; Power-added efficiency; Dynamic drain current.

Figure notes: TA=+25°C; Vd=+28V; Vg=-2.6V. Readings are approximate. Pin = 24 dBm. PAE is plotted in percent.







Document notice

Specifications are subject to revision. Before design release, confirm the current document revision and product-specific requirements with LCRF. The approved quotation and order documentation define the supplied configuration.