6700–7400 MHz GaN MMIC Power Amplifier Die

LCI-PA-6700-7400-PS41T is a GaN MMIC power amplifier supplied as bare die for 6700–7400 MHz. It supports pulsed operation.

The RF table specifies: Saturated output power: 41 dBm (typical); Power gain: 22 dB (typical); Power-added efficiency: 55 % (typical). RF test conditions: TA = +25 °C; Vd = +28 V; Vg = -2.6 V. Die dimensions: 3.2 × 2 × 0.1 mm.



RF characteristics

TA = +25 °C; Vd = +28 V; Vg = -2.6 V

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Operating frequency	f	6700	-	7400	MHz
Saturated output power	Psat	-	41	-	dBm
Power gain	Gp	-	22	-	dB
Power-gain flatness	ΔGp	-	-	±0.5	dB
Power-added efficiency	PAE	-	55	-	%
Small-signal gain	S21	-	34	-	dB
Small-signal gain flatness	ΔS21	-	-	±0.5	dB
Input VSWR	VSWR(in)	-	1.6	1.8	-

DC bias and current

TA = +25 °C

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Drain voltage	Vd	-	28	32	V
Gate voltage	Vg	-3.1	-2.8	-2.6	V
Quiescent drain current	Id	-	0.38	-	A
Dynamic drain current	Idd	-	0.8	-	A
Dynamic gate current	Igg	-	0.1	0.3	mA

Absolute limits

Absolute limits; these are not normal operating targets.

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Maximum drain-source voltage	Vd	-	-	32	V
Minimum gate bias	Vg	-5	-	-	V
Maximum input power	Pin	-	-	24	dBm
Maximum channel temperature	Top	-	-	225	°C
Storage temperature	TSTG	-65	-	150	°C

Die dimensions

Parameter	Value	Unit
Die dimensions	3.2 × 2 × 0.1	mm
Plan-view outline tolerance	±100 (Plan-view outline only; the source does not specify thickness tolerance.)	µm

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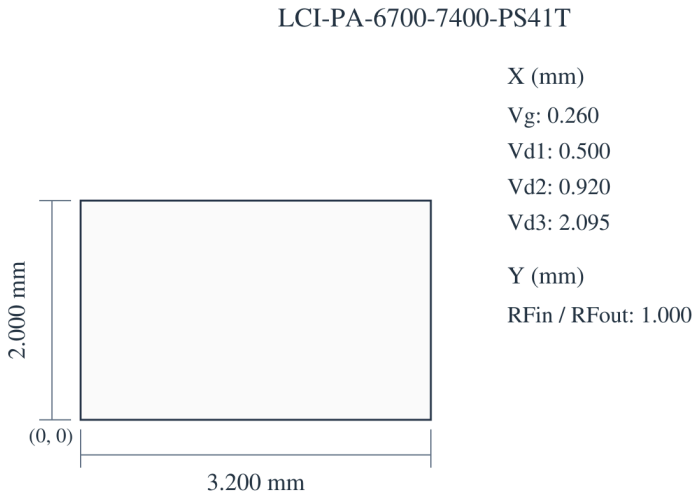
Die integration and bias sequencing

Apply gate bias before drain bias at power-up. At power-down, remove drain bias before gate bias. Use clean handling, ESD protection and a carrier with thermal expansion compatible with the SiC die.

Mechanical Outline

Outline and dimensioned coordinates in mm. Use this drawing as a dimensional reference. Fabrication and wire bonding require complete drawings.

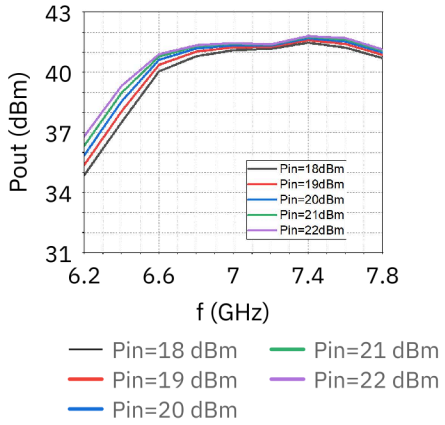


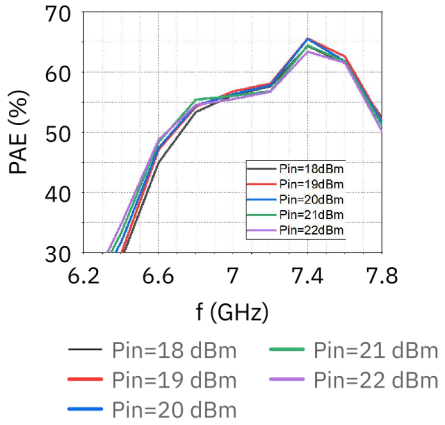
Performance curves

Each performance plot applies to its stated operating conditions.

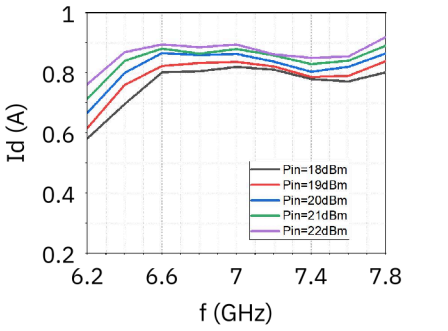
Pulsed — Saturated output power; Power-added efficiency; Dynamic drain current; Input matching; Gain.

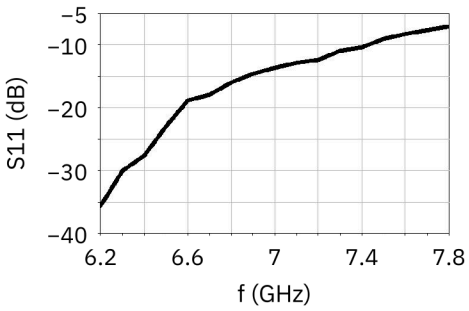
Figure notes: TA = +25°C; Vd=+28V; Vg=-2.8V. τ = 100 µs; D = 10%; Pout / PAE / Id: Pin = 18 / 20 / 22 dBm; S11 / S21: Pin = −10 dBm. Conditions apply to the indicated plots and their own legends.





Pin=18 dBm Pin=21 dBm
Pin=19 dBm Pin=22 dBm
Pin=20 dBm





Pin=18 dBm Pin=21 dBm
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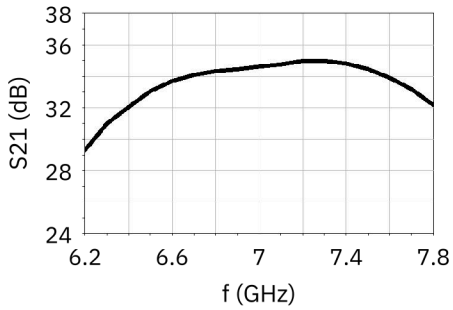
Pin=20 dBm



Document notice

Specifications are subject to revision. Before design release, confirm the current document revision and product-specific requirements with LCRF. The approved quotation and order documentation define the supplied configuration.