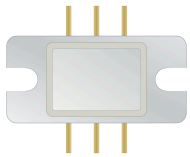
2500–6200 MHz GaN Packaged Power Amplifier — PSAT 44.5 dBm (typical)

LCI-PA-2500-6200-PS44p5T · 2500–6200 MHz · GaN Packaged Power Amplifier.



The RF table specifies: PSAT 44.5 dBm (typical); Power gain 16.5 dB (typical); PAE 40 % (typical). RF test conditions: TA = +25 °C; Vd = +28 V; Vg = -2.8 V; Pin = 28 dBm; PW = 100 µs; D = 10%.

CMW010B

Apply gate bias before drain bias. Remove drain bias before gate bias.

Vg: C = 10 µF

Vg: 1, 3

RFin: 2

Vd: 4, 6

RFout: 5

Package outline dimensions: 18.03 × 8.7 mm.

RF characteristics

TA = +25 °C; Vd = +28 V; Vg = -2.8 V; Pin = 28 dBm; PW = 100 µs; D = 10%

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Operating frequency	f	2500	-	6200	MHz
Saturated output power	-	44	44.5	-	dBm
Power gain	-	16	16.5	-	dB
Power-added efficiency	-	35	40	-	%
Power-gain flatness	-	-	-	±0.5	dB
Small-signal gain	-	24	25	27	dB
Small-signal gain flatness	-	-	-	±2	dB
Input VSWR	-	-	1.8	2	-

DC bias and current

Parameter	Symbol	Minimum	Typical	Maximum	Value	Unit
Drain voltage	-	-	-	-	28	V
Gate voltage	-	-	-	-	-2.8	V

Absolute limits

Absolute limits; these are not normal operating targets.

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Maximum drain-source voltage	-	-	-	32	V
Minimum gate bias	-	-5	-	-	V

Absolute limits

Absolute limits; these are not normal operating targets.

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Storage temperature	-	-65	-	150	°C
Maximum input power	-	-	-	30	dBm
Maximum channel temperature	-	-	-	200	°C

Package outline dimensions

Parameter	Value	Unit
Package outline dimensions	18.03 × 8.7	mm
Terminal span	14.7	mm

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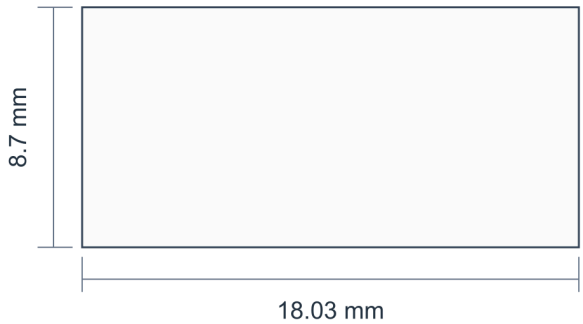
Integration notes

CMW010B Apply gate bias before drain bias. Remove drain bias before gate bias. Vg: C = 10 µF Vg: 1, 3 RFin: 2 Vd: 4, 6 RFout: 5

Mechanical Outline

Partial drawing showing confirmed dimensions in mm. Use the complete installation drawing for installation.

LCI-PA-2500-6200-PS44p5T



18.03 × 8.7 mm

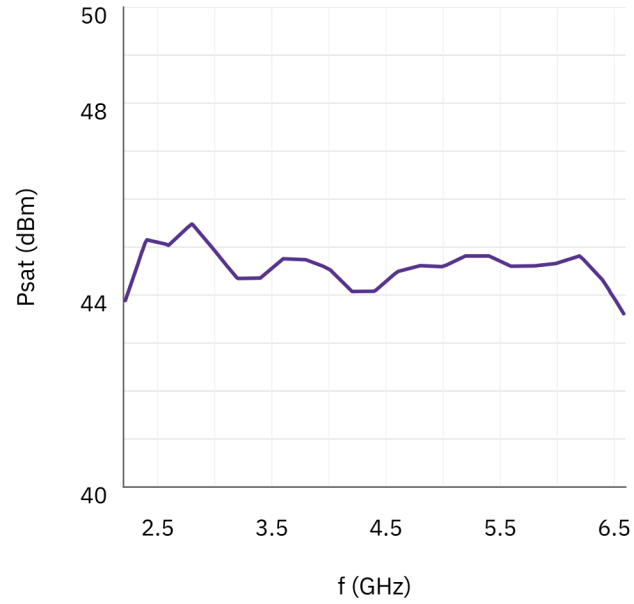
mm

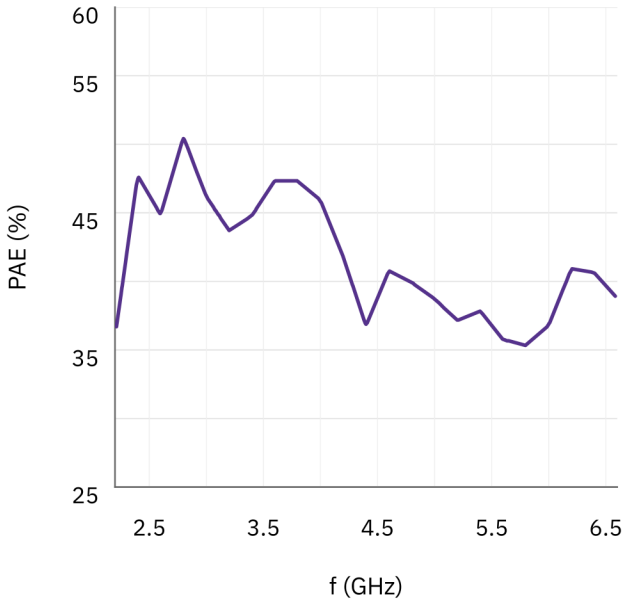
Performance curves

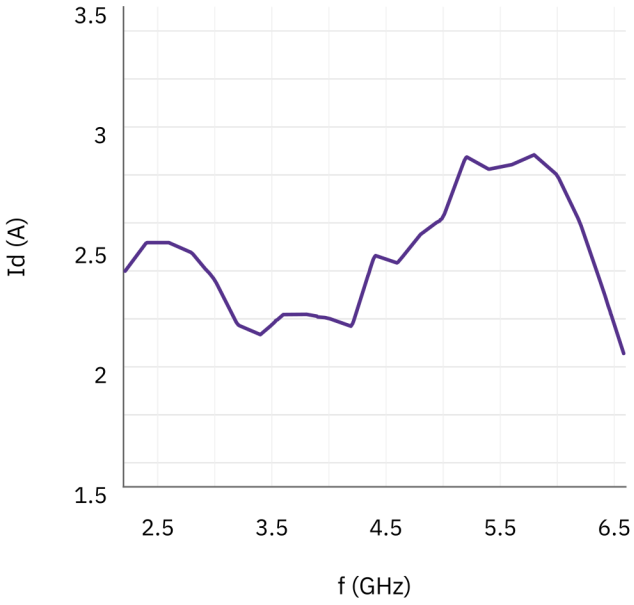
Each performance plot applies to its stated operating conditions.

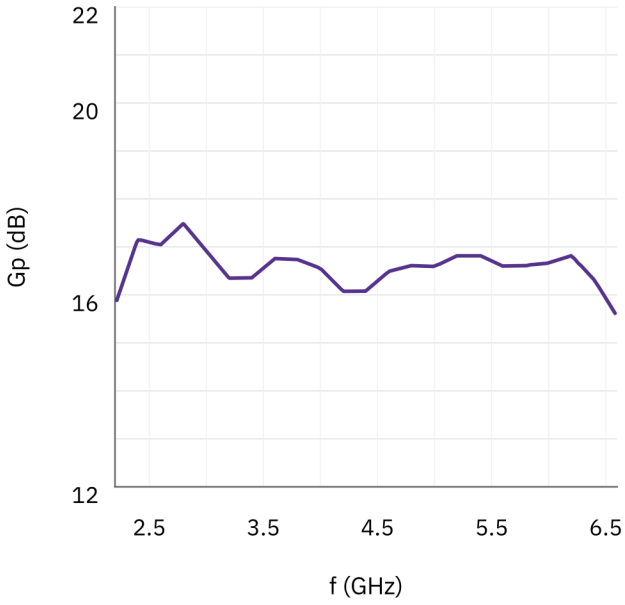
Pulsed — Saturated output power; Dynamic drain current; Power-added efficiency; Power gain.

Figure notes: TA=+25°C; Vd=+28V; Vg=-2.8V; Pin=28dBm. Readings are approximate. τ = 100 µs; D = 10%.









Document notice

Specifications are subject to revision. Before design release, confirm the current document revision and product-specific requirements with LCRF. The approved quotation and order documentation define the supplied configuration.