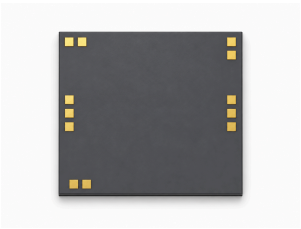
2500–3700 MHz GaN MMIC Power Amplifier Die

LCI-PA-2500-3700-PS41p5T is a GaN MMIC power amplifier supplied as bare die for 2500–3700 MHz. It supports CW and pulsed operation.



The RF table specifies: PSAT 41.5 dBm (typical); Power gain 22.5 dB (typical); PAE 50% (typical). RF test conditions: TA = +25 °C; Vd = +28 V; Vg = -2.8 V; Pin = 19 dBm; τ = 100 µs; D = 10%.

Die dimensions: 2.22 × 1.99 mm. Thickness is not specified.

RF characteristics

TA = +25 °C; Vd = +28 V; Vg = -2.8 V; Pin = 19 dBm; τ = 100 µs; D = 10%

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Operating frequency	f	2500	-	3700	MHz
Saturated output power	Psat	41	41.5	-	dBm
Power gain	Gp	22	22.5	-	dB
Power-gain flatness	ΔGp	-	-	1	dB
Power-added efficiency	PAE	48	50	-	%
Small-signal gain	S21	30	31	32	dB
Small-signal gain flatness	ΔS21	-	-	±1	dB
Input VSWR	VSWR(in)	-	1.8	2	-

DC bias and current

Parameter	Symbol	Minimum	Typical	Maximum	Value	Unit
Drain voltage	Vd	-	28	-	-	V
Gate voltage	Vg	-	-2.8	-	-	V
Quiescent drain current	Id	-	-	-	0.17 (Vd = +28 V)	A

Absolute limits

Absolute limits; these are not normal operating targets.

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Maximum drain-source voltage	Vd	-	-	32	V
Minimum gate bias	Vg	-5	-	-	V
Maximum input power	Pin	-	-	23	dBm
Storage temperature	TSTG	-65	-	150	°C
Maximum channel temperature	Top	-	-	200	°C

Die dimensions

Parameter	Value	Unit
Die dimensions	2.22 × 1.99	mm
Plan-view outline tolerance	±100 (Plan-view outline only; the source does not specify thickness tolerance.)	µm

Current publication record · 1.7 · 2026-11-12. Product description or editorial content updated. Replaces revision 1.6.

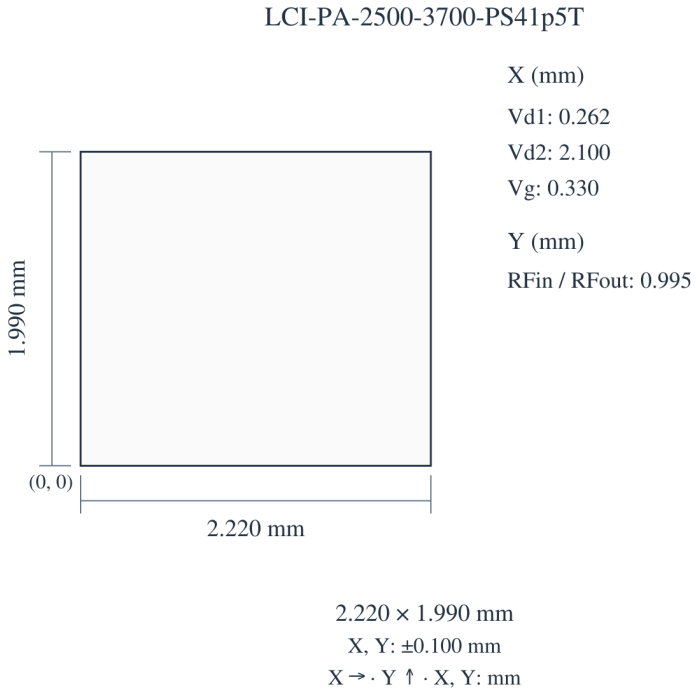
Die integration and bias sequencing

Integrated DC blocking: RFin / RFout. DC path to ground: RFin → GND. Apply gate bias before drain bias. Remove drain bias before gate bias. Outline and dimensioned coordinates in mm. Use this drawing as a dimensional reference. Fabrication and wire bonding require complete drawings.

Bond-pad dimensions

RFin: 100 × 100 µm. Vg: 100 × 100 µm. Vd1: 120 × 120 µm. Vd2: 120 × 120 µm. RFout: 120 × 120 µm.

Mechanical Outline

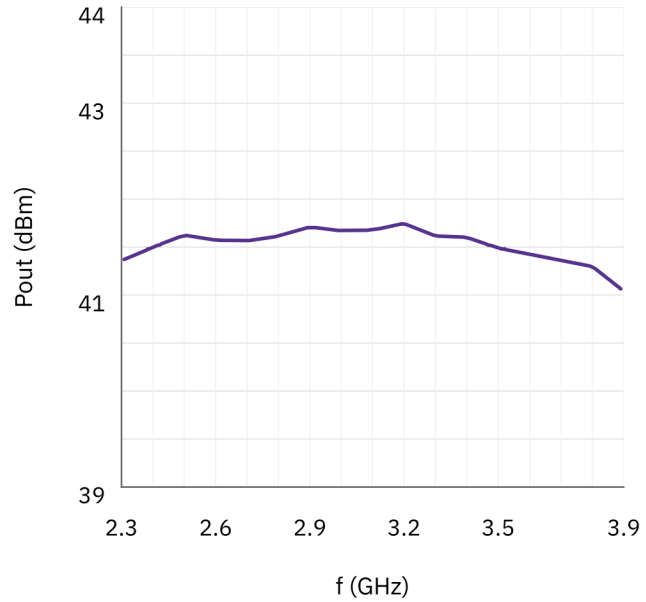


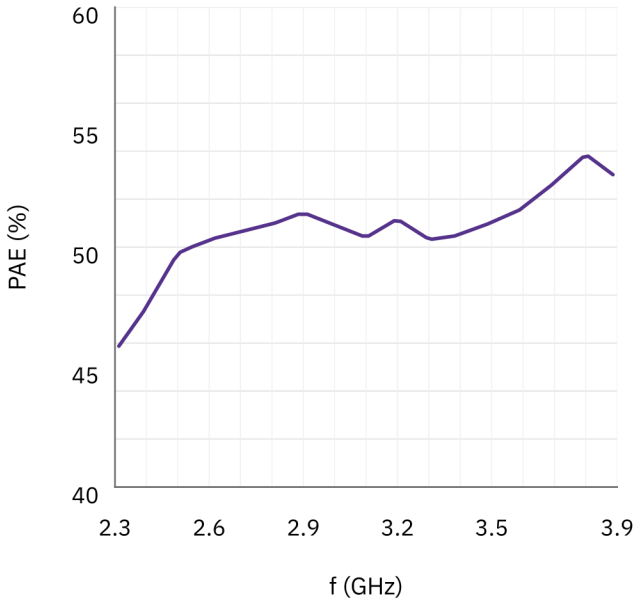
Performance curves

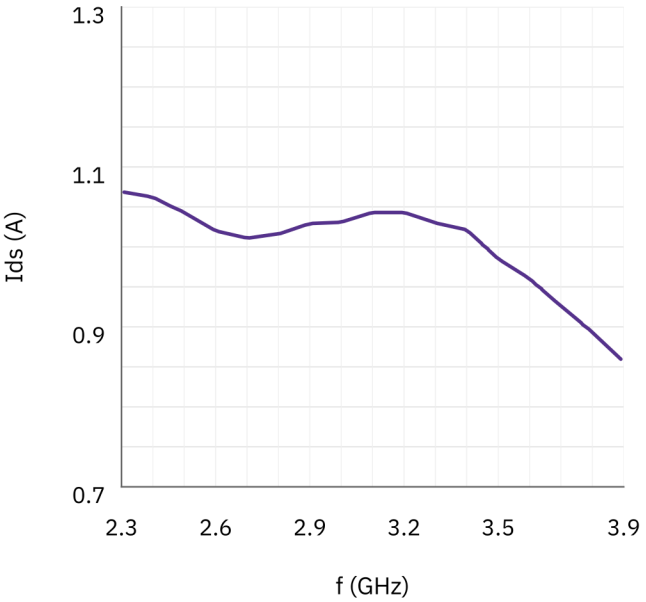
Read each figure with its stated operating conditions.

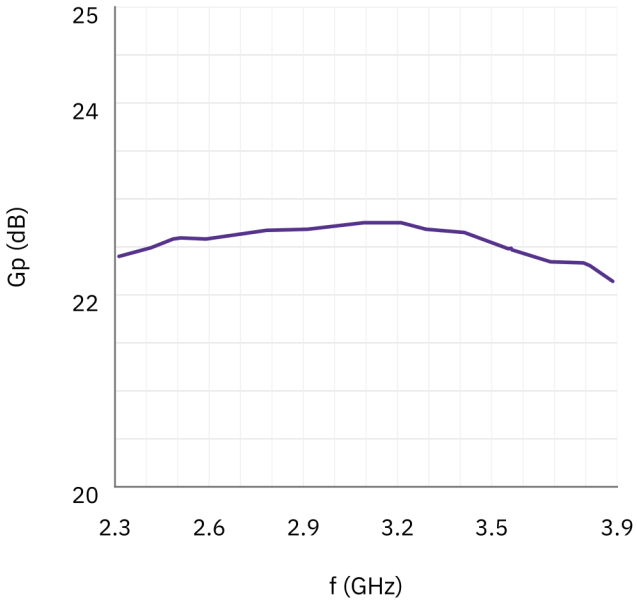
Pulsed — Saturated output power; Dynamic drain current; Power-added efficiency; Power gain.

Figure notes: TA=+25°C; Vd=+28V; Vg=-2.8V; Pin=19dBm. Pulse width = 100 µs; duty cycle = 10%. Readings are approximate.



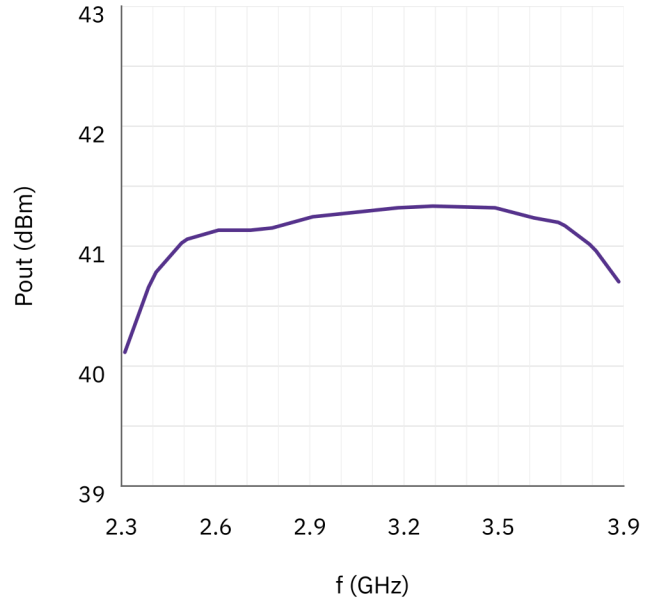


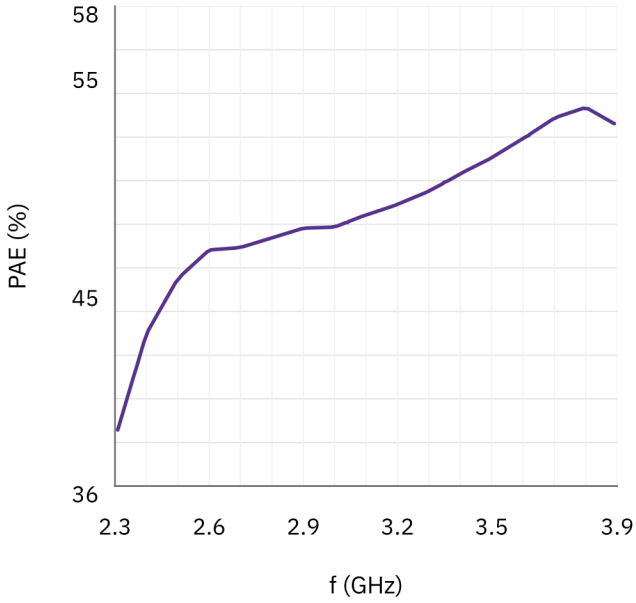


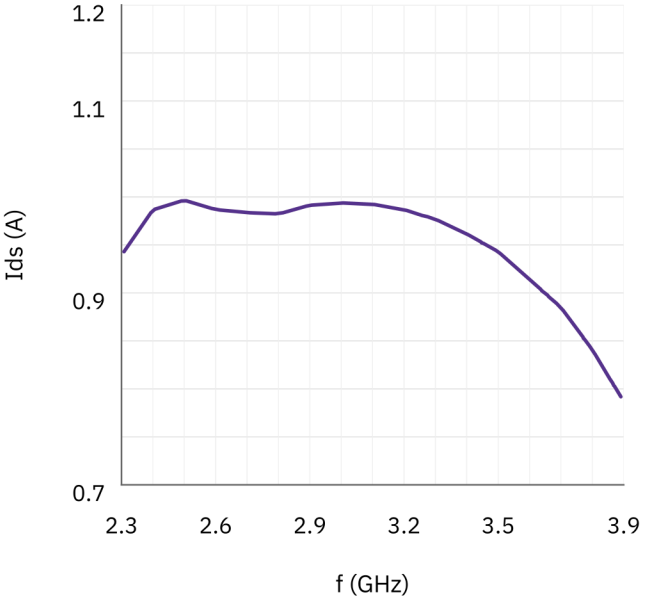


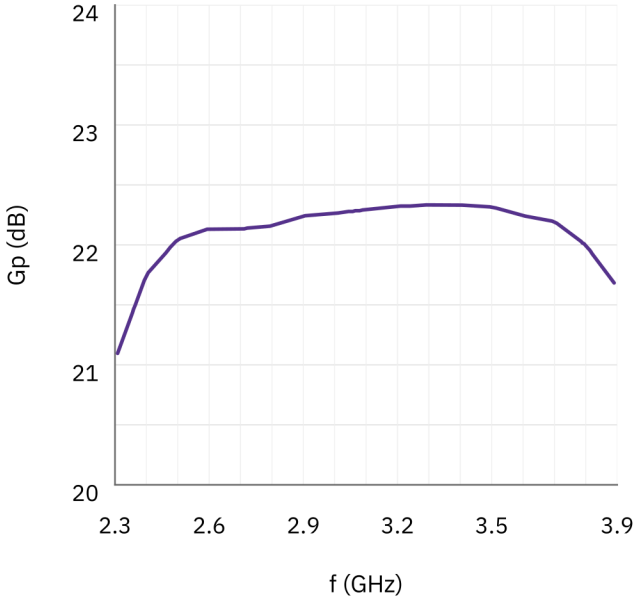
Continuous wave (CW) — Saturated output power; Dynamic drain current; Power-added efficiency; Power gain.

Figure notes: TA=+25°C; Vd=+28V; Vg=-2.8V; Pin=19dBm. Readings are approximate.









Document notice

Specifications are subject to revision. Before design release, confirm the current document revision and product-specific requirements with LCRF. The approved quotation and order documentation define the supplied configuration.