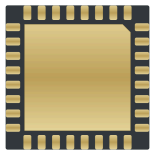
10–1100 MHz GaN Packaged Power Amplifier — PSAT ≥ 40 dBm

LCI-PA-10-1100-PS40MIN · 10–1100 MHz · GaN Packaged Power Amplifier.

The RF table specifies: PSAT ≥ 40 dBm; Power gain ≥ 13 dB; PAE ≥ 60 %. RF test conditions: TA = +25 °C; VDS = +28 V; VGS ∈ [-3.2, -2.4] V; Pin = 23 dBm; CW.



QFN32 5 × 5 mm

Apply gate bias before drain bias. Remove drain bias before gate bias.

Vg: C = 10 µF

T_peak ≤ 260 °C

RFin / Vg: 4, 5

RFout / Vd: 20, 21

GND: 1, 8, 9, 16, 17, 24, 25, 32

Package outline dimensions: 5 × 5 × 1 mm.

RF characteristics

TA = +25 °C; VDS = +28 V; VGS ∈ [-3.2, -2.4] V; Pin = 23 dBm; CW

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Operating frequency	f	10	-	1100	MHz
Saturated output power	-	40	-	-	dBm
Power gain	-	13	-	-	dB
Power-added efficiency	-	60	-	-	%
Power-gain flatness	-	-	-	±0.5	dB

DC bias and current

Parameter	Symbol	Minimum	Typical	Maximum	Value	Unit
Drain voltage	-	-	-	-	28	V
Gate voltage	-	-	-	-	[-3.2, -2.4]	V

Absolute limits

Absolute limits; these are not normal operating targets.

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Maximum drain-source voltage	-	-	-	32	V
Minimum gate bias	-	-5	-	-	V
Storage temperature	-	-65	-	150	°C
Maximum input power	-	-	-	27	dBm

Package outline dimensions

Parameter	Value	Unit
Package outline dimensions	5 × 5 × 1	mm

Pin assignments

Symbol	Pins	Function
GND	1,8,9,16,17,24,25,32	-
NIC	2,3,6,7,10,11,12,13,14,15,18,19,22,23, 26,27,28,29,30,31	Not internally connected; may be grounded
RFin	4,5	RF input / gate bias
RFout	20,21	RF output / drain bias

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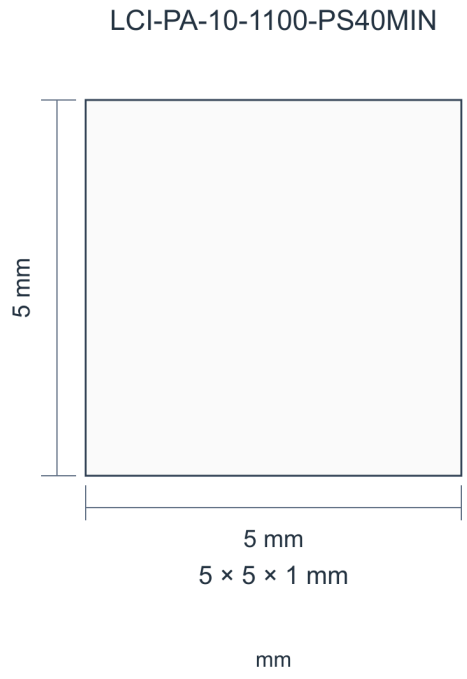
Integration notes

QFN32 5 × 5 mm Apply gate bias before drain bias. Remove drain bias before gate bias. Vg: C = 10 µF T_peak ≤ 260 °C RFin / Vg: 4, 5 RFout / Vd: 20, 21 GND: 1, 8, 9, 16, 17, 24, 25, 32 The exposed pad on the package underside must connect to RF and DC ground.

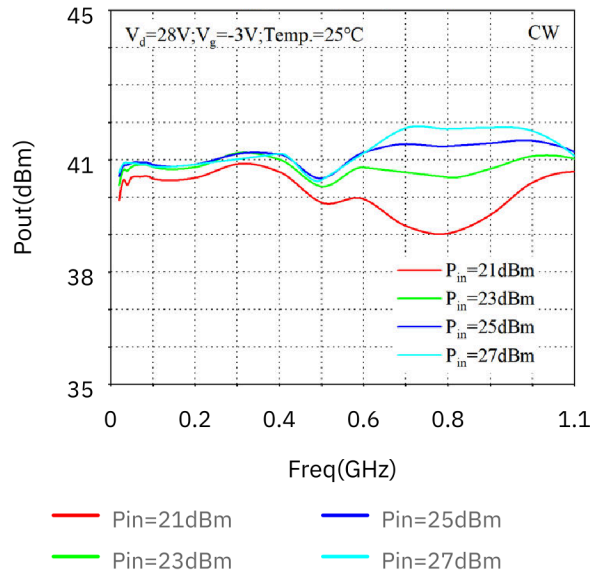
Mechanical Outline

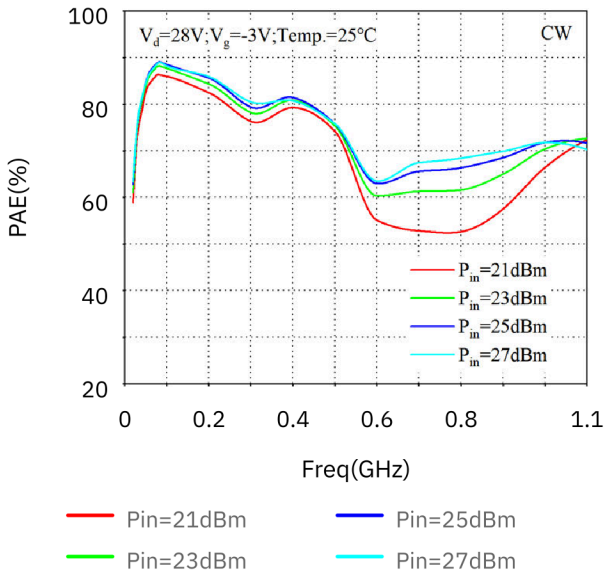
Partial drawing showing confirmed dimensions in mm. Use the complete installation drawing for installation.

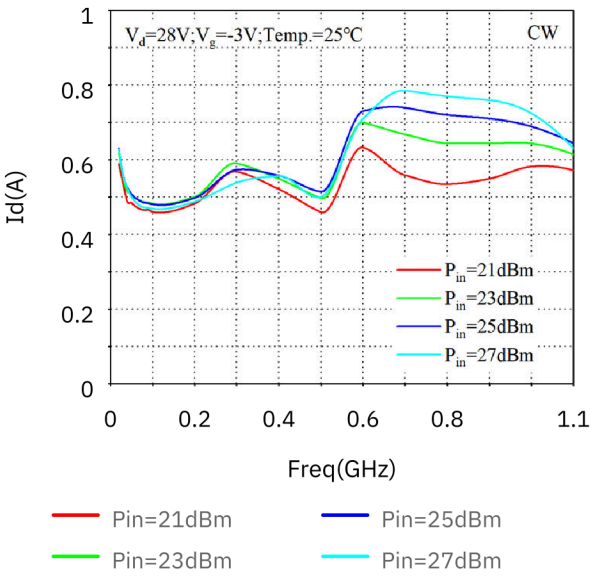


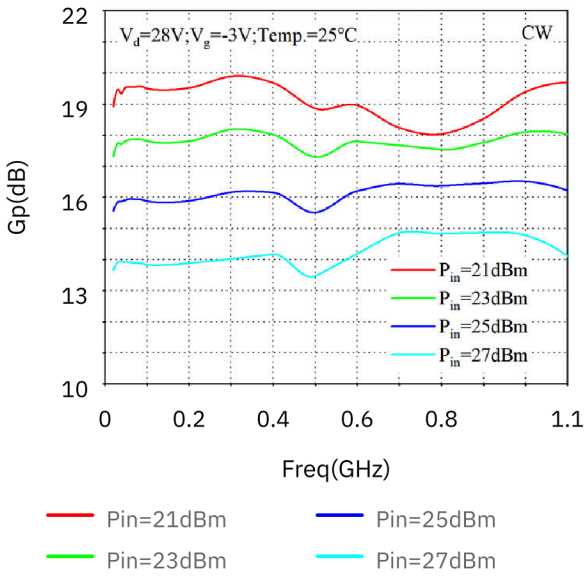
Performance curves

Each performance plot applies to its stated operating conditions.









Output power; Power-added efficiency; Drain current; Power gain. Vd = 28 V; Vg = -3 V; TA = 25 °C; CW; Pin = 21/23/25/27 dBm.

Document notice

Specifications are subject to revision. Before design release, confirm the current document revision and product-specific requirements with LCRF. The approved quotation and order documentation define the supplied configuration.