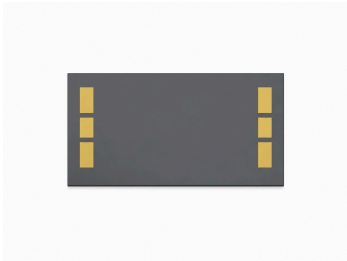


6000–18000 MHz GaAs PIN RF Limiter Die

Insertion loss: 0.6 dB (typical); Limiting level: 16 dBm (typical) / 17 dBm (maximum); Maximum pulsed input power: 41 dBm (maximum) ($\tau = 300\ \mu\text{s}$; D = 30%). Die dimensions: $1.6 \times 0.85 \times 0.1\ \text{mm}$.



RF characteristics · TA = +25 °C

Parameter	Symbol	Minimum	Typical	Maximum	Value	Unit
Operating frequency	-	6000	-	18000	6000–18000	MHz
Insertion loss	-	-	0.6	-	-	dB
Input VSWR	-	-	1.5	-	-	-
Output VSWR	-	-	1.5	-	-	-
Limiting level	-	-	16	17	-	dBm
Maximum pulsed input power	-	-	-	41	$\tau = 300\ \mu\text{s}$; D = 30%	dBm

Environmental specifications

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Storage temperature	-	-65	-	150	°C
Operating temperature	-	-55	-	125	°C

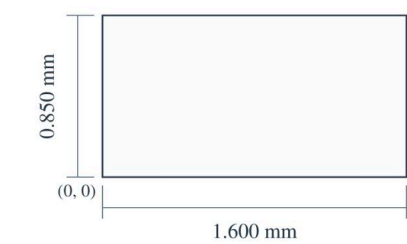
Die dimensions

Parameter	Value	Unit
Die dimensions	1.6 × 0.85 × 0.1	mm

Mechanical Outline

LCI-LIM-6000-18000-PIP41MAX

Y (mm)
RF_in (2) / RF_out (5): 0.450



1.600 × 0.850 × 0.100 mm
X, Y: ±0.100 mm
X → · Y ↑ · X, Y: mm

All dimensions are in mm [in].

Bond-pad assignments

Pins	Symbol	Function	Notes
2	RF_in	RF input	50 Ω
5	RF_out	RF output	50 Ω
1, 3, 4, 6	GND	Ground pad for probe testing	-

Current publication record · 1.3 · 2026-10-27. Product description or editorial content updated. Replaces revision 1.2.

Die integration and bias sequencing

Input and output DC-blocking capacitors are integrated.

Mechanical reference scope

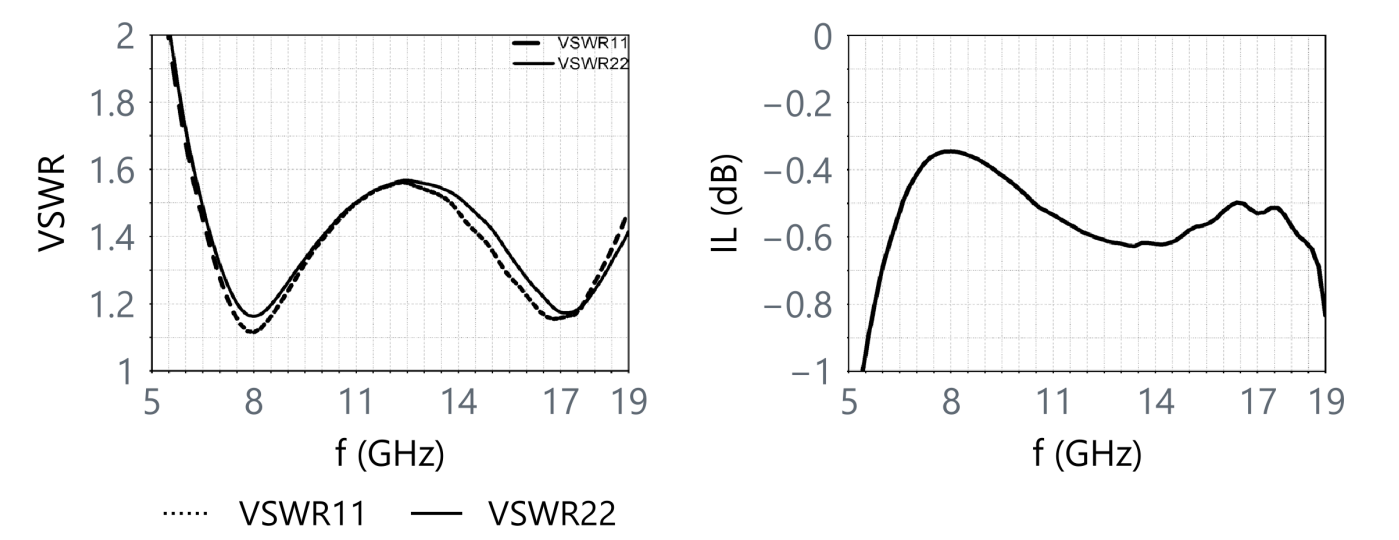
Source-based dimensional guide in mm. Partial information; not a complete fabrication or wire-bonding drawing.

Performance curves

Plots from the source datasheet. Read each figure with its stated operating conditions.

Input/output matching; Insertion loss.

Source-page annotation: TA=+25°C. The displayed source plot bodies, including curves and grid, are retained; only exterior axes and layout are reformatted. No measurement arrays or hidden states are reconstructed. The full original frequency-axis extent, 5–19 GHz, is retained.



[Source](#)

Document notice

Specifications are subject to revision. Before design release, confirm the current document revision and product-specific requirements with LCRF. The approved quotation and order documentation define the supplied configuration.