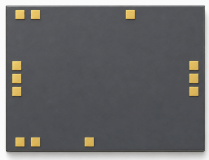
8000–18000 MHz GaAs MMIC Bidirectional Amplifier Die — Rx / Tx P1dB 24 dBm (typical)



LCI-BDA-8000-18000-RXP1DB24T · 8000–18000 MHz · GaAs MMIC Bidirectional Amplifier Die.

The RF table specifies: Rx / Tx: Rx / Tx P1dB 24 dBm (typical); Small-signal gain 21 dB (typical). RF test conditions: TA = +25 °C; Vd = +5 V.

Input and output DC-blocking capacitors are integrated.

The RF pads do not require external DC-blocking capacitors; add the specified supply filtering at VDD.

Die dimensions: 2.5 × 1.9 × 0.1 mm.

Rx: VdRx = 5 V; VdTx = 0 V; Vcc = 5 V; VEE = -5 V

Tx: VdRx = 0 V; VdTx = 5 V; Vcc = 0 V; VEE = -5 V

RF characteristics

TA = +25 °C; Vd = +5 V

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Operating frequency	f	8000	-	18000	MHz
Output 1 dB compression power (Rx)	-	-	24	-	dBm
Saturated output power (Rx)	-	-	24.5	-	dBm
Small-signal gain (Rx)	-	-	21	-	dB
Small-signal gain flatness (Rx)	-	-	±1	-	dB
Noise figure (Rx)	-	-	8	-	dB
Input VSWR (Rx)	-	-	1.3	-	-
Output VSWR (Rx)	-	-	1.4	-	-
Output 1 dB compression power (Tx)	-	-	24	-	dBm
Saturated output power (Tx)	-	-	24.5	-	dBm
Small-signal gain (Tx)	-	-	21	-	dB
Small-signal gain flatness (Tx)	-	-	±1	-	dB
Noise figure (Tx)	-	-	8	-	dB
Input VSWR (Tx)	-	-	1.3	-	-
Output VSWR (Tx)	-	-	1.4	-	-

DC bias and current

TA = +25 °C

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Operating voltage	-	-	5	-	V
Quiescent current (Rx)	-	-	0.22	-	A
Quiescent current (Tx)	-	-	0.22	-	A

Absolute limits

Absolute limits; these are not normal operating targets.

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Maximum operating voltage	-	-	-	6	V
Maximum input power	-	-	-	10	dBm
Storage temperature	-	-65	-	150	°C
Maximum channel temperature	-	-	-	175	°C

Die dimensions

Parameter	Value	Unit
Die dimensions	2.5 × 1.9 × 0.1	mm
Plan-view outline tolerance	±50	µm

Bond-pad assignments

Pins	Symbol	mm
1	Tx_in / Rx_out	0.100 × 0.140
2	Tx_out / Rx_in	0.100 × 0.140
3	VdTx	0.100 × 0.100
4	VdRx	0.100 × 0.100
5, 7	Vcc	0.100 × 0.100
6, 8	VEE	0.100 × 0.100
9–12	GND	0.100 × 0.100

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Integration notes

Input and output DC-blocking capacitors are integrated. The RF pads do not require external DC-blocking capacitors; add the specified supply filtering at VDD. Rx: VdRx = 5 V; VdTx = 0 V; Vcc = 5 V; VEE = -5 V Tx: VdRx = 0 V; VdTx = 5 V; Vcc = 0 V; VEE = -5 V

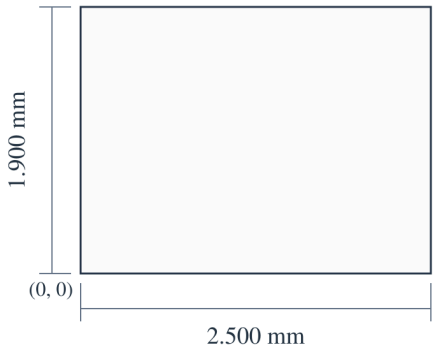
Pad connection notes

The listed GND pads are test ground pads.

Mechanical Outline

Outline and dimensioned coordinates in mm. Use this drawing as a dimensional reference. Fabrication and wire bonding require complete drawings.

LCI-BDA-8000-18000-RXP1DB24T



X (mm)
Vcc (5): 0.215
Vcc (7): 0.190
VEE (6, 8): 0.390
VdTx: 1.540
VdRx: 1.036

Y (mm)
Tx / Rx: 0.943

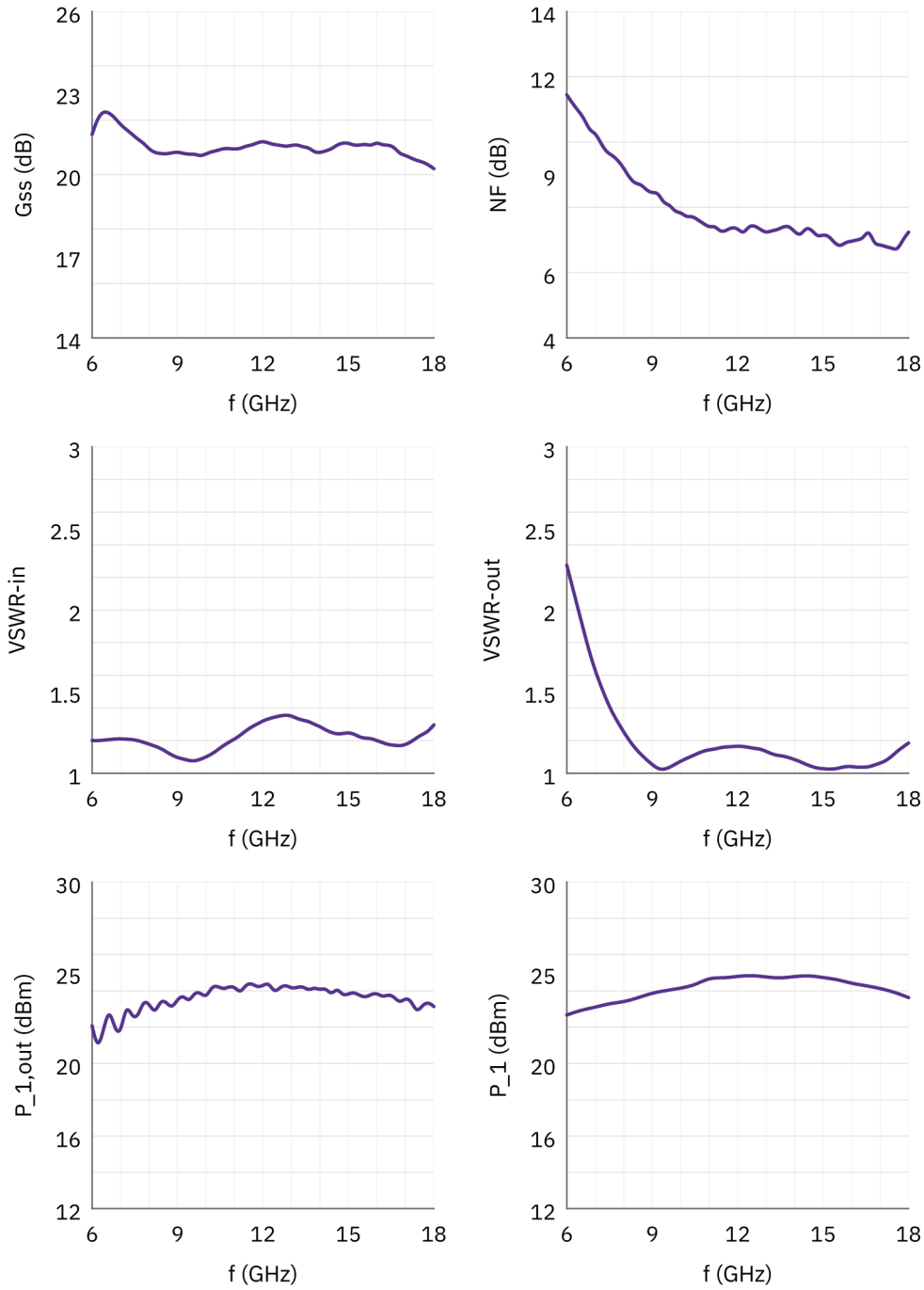
2.500 × 1.900 × 0.100 mm
X, Y: ±0.050 mm
X → · Y ↑ · X, Y: mm

Performance curves

Each performance plot applies to its stated operating conditions.

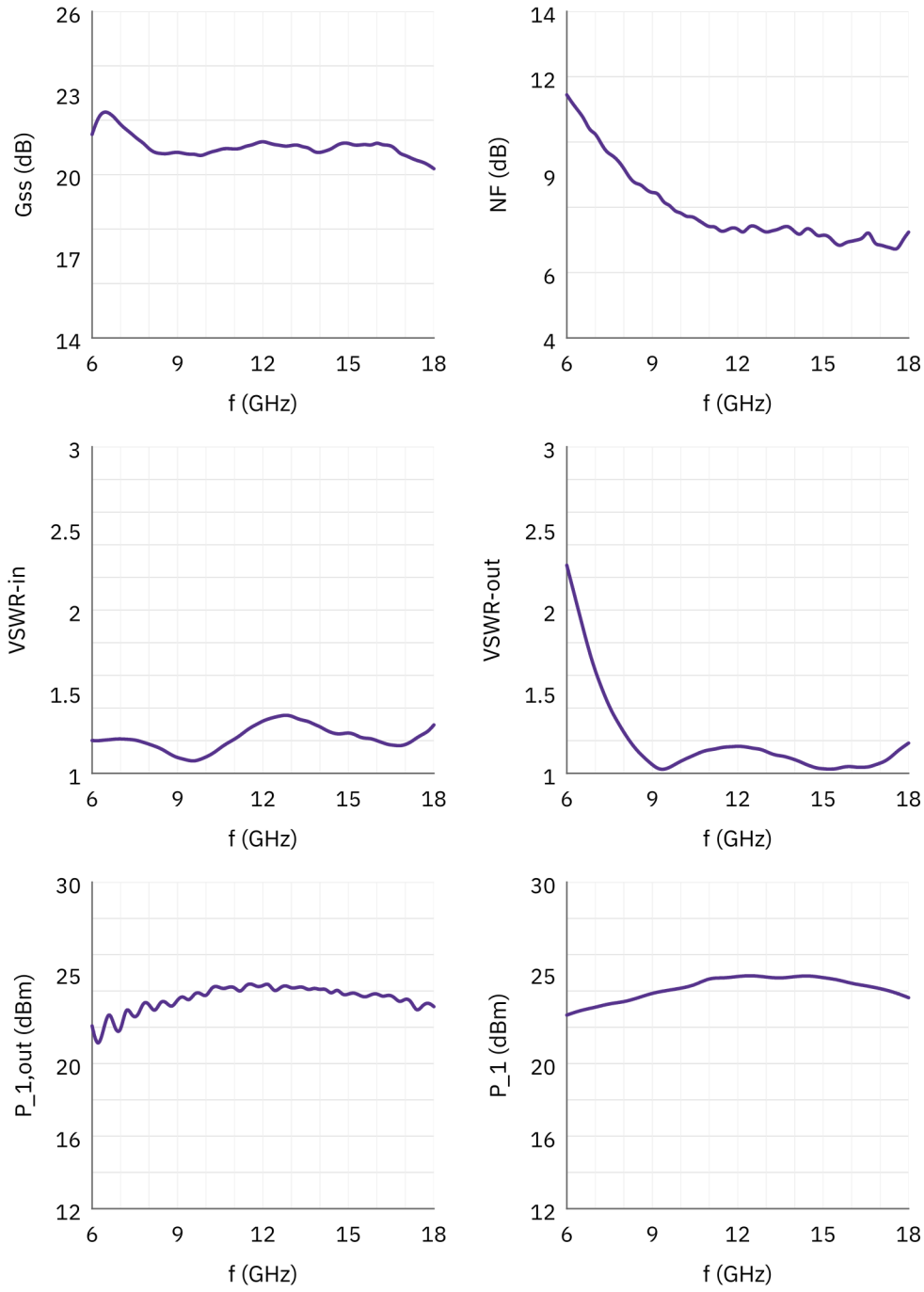
Rx — Small-signal gain; Noise figure; Input VSWR; Output VSWR; P_1,out; P_1.

Figure notes: Vd = +5 V; Pin = −30 dBm. Readings are approximate. The final plot shows P_1. The values are not specified as saturated output power or 1 dB compression points.



Tx — Small-signal gain; Noise figure; Input VSWR; Output VSWR; P_1,out; P_1.

Figure notes: Vd = +5 V; Pin = −30 dBm. Readings are approximate. The final plot shows P_1. The values are not specified as saturated output power or 1 dB compression points.



Document notice

Specifications are subject to revision. Before design release, confirm the current document revision and product-specific requirements with LCRF. The approved quotation and order documentation define the supplied configuration.