2700–3500 MHz GaAs MMIC Amplifier Die — P1dB 23.5 dBm (typical)

LCI-AMP-2700-3500-P1DB23p5T · 2700–3500 MHz · GaAs MMIC Amplifier Die.



The RF table specifies: P1dB 23.5 dBm (typical); Small-signal gain 28 dB (typical); NF 3.5 dB (typical). RF test conditions: TA = +25 °C; Vd = +5 V.

Single-supply operation.

Input and output DC-blocking capacitors are integrated.

Die dimensions: 2.6 × 1 × 0.1 mm.

RF characteristics

TA = +25 °C; Vd = +5 V

Parameter	Symbol	Minimum	Typical	Maximum	Value	Unit
Operating frequency	f	2700	-	3500	-	MHz
Output 1 dB compression power	P1dB	-	23.5	-	-	dBm
Saturated output power	Psat	-	-	-	25	dBm
Small-signal gain	Gain	-	28	-	-	dB
Small-signal gain flatness	ΔGain	-	±0.3	-	-	dB
Noise figure	NF	-	3.5	-	-	dB
Input VSWR	VSWR-in	-	1.3	-	-	-
Output VSWR	VSWR-out	-	1.5	-	-	-

DC bias and current

TA = +25 °C

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Operating voltage	Vd	-	5	-	V
Quiescent current	Id	-	0.165	-	A

Absolute limits

Absolute limits; these are not normal operating targets.

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Maximum operating voltage	-	-	-	6	V
Maximum input power	-	-	-	15	dBm
Storage temperature	-	-65	-	150	°C
Maximum channel temperature	-	-	-	175	°C

Die dimensions

Parameter	Value	Unit
Die dimensions	2.6 × 1 × 0.1	mm
Plan-view outline tolerance	±100	µm

Bond-pad assignments

Pins	Symbol	mm
1	RFin	0.120 × 0.100
2	RFout	0.120 × 0.100
3	Vd	0.100 × 0.100
4, 5, 6, 7	GND	0.100 × 0.100

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Integration notes

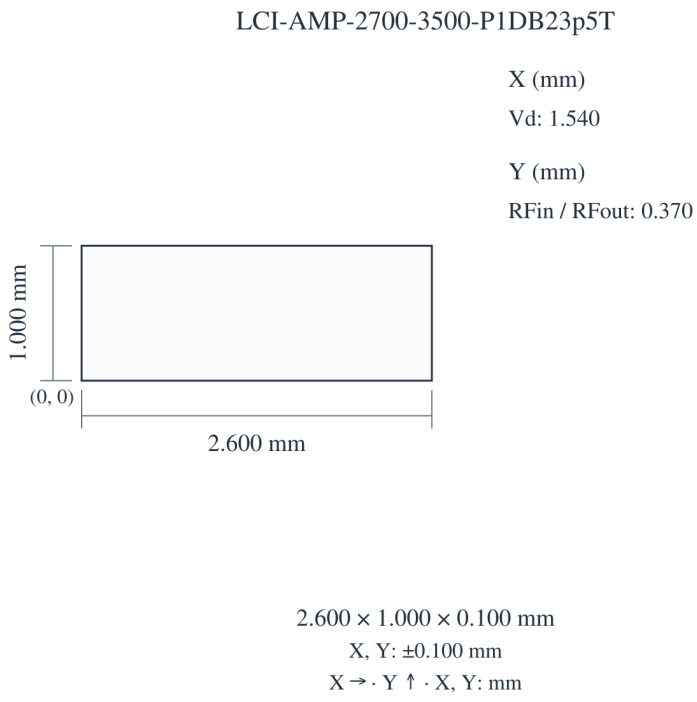
Single-supply operation. Input and output DC-blocking capacitors are integrated.

Pad connection notes

The listed GND pads are test ground pads.

Mechanical Outline

Outline and dimensioned coordinates in mm. Use this drawing as a dimensional reference. Fabrication and wire bonding require complete drawings.



2.600 × 1.000 × 0.100 mm
X, Y: ±0.100 mm
X → · Y ↑ · X, Y: mm

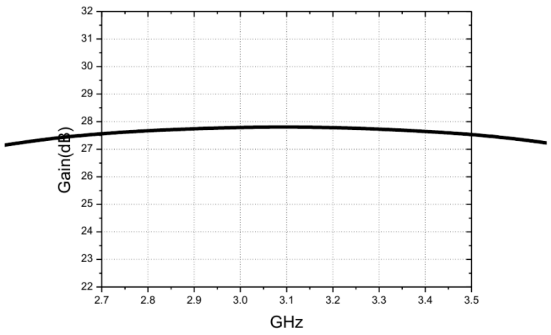
Performance curves

Each performance plot applies to its stated operating conditions.

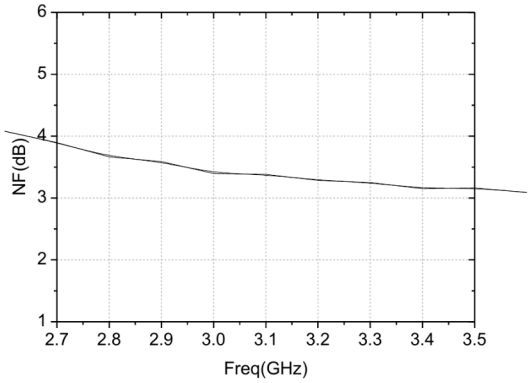
Output 1 dB compression power; Small-signal gain; Input matching; Output matching; Noise figure; Saturated output power.

Figure notes: Vd=+5V; Pin=-20dBm.

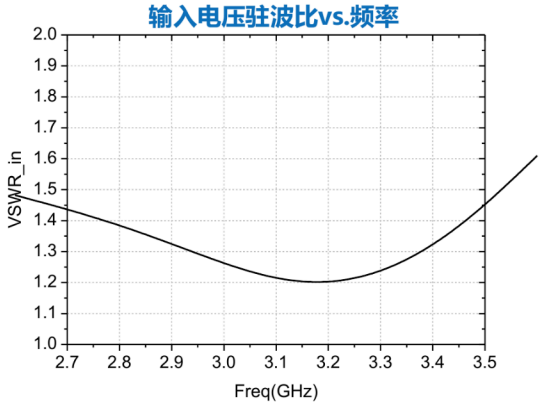
小信号增益vs.频率



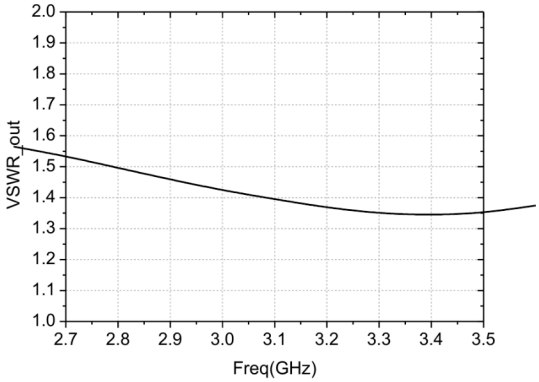
噪声系数vs.频率



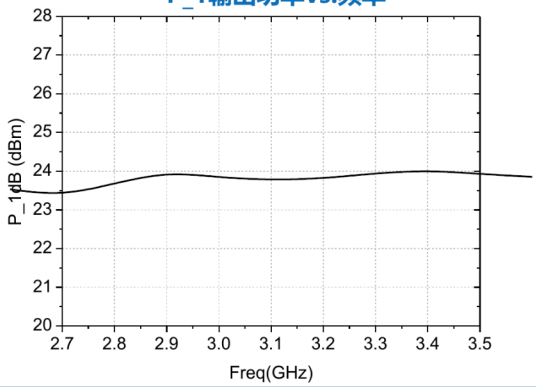
输入电压驻波比vs.频率



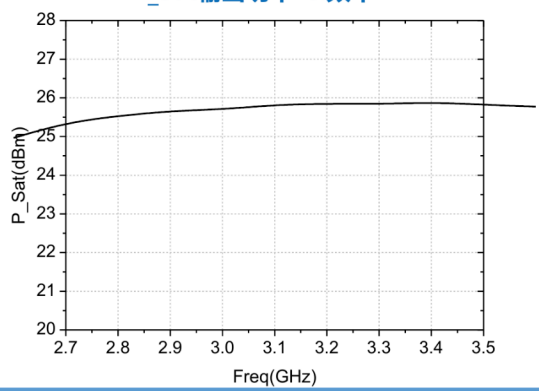
输出电压驻波比vs.频率



P_1输出功率vs.频率



P_Sat输出功率vs.频率



Document notice

Specifications are subject to revision. Before design release, confirm the current document revision and product-specific requirements with LCRF. The approved quotation and order documentation define the supplied configuration.